ABSTRACT OF THE DISCLOSURE

A display capable of suppressing the current consumption is disclosed. The display comprises a plurality of stages of shift register circuits for sequentially driving a plurality of drain lines and a plurality of stages of first dummy shift register circuits arranged on the operation starting side of the plurality of stages of shift register circuits and not connected to the drain lines. The first dummy shift register circuits include a first transistor connected to a first potential, a second transistor connected to a second potential, and a third transistor for turning off the first transistor when the second transistor is in on state.